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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Ira J. Schultz.

The application has been amended as follows:

Claims 1-10 (canceled)

11. (new) An installation <u>system</u> for the high-speed acquisition of acquisition data including an Ethernet network with a plurality of nodes (N), where at least one of the nodes of the Ethernet network constitutes a client/server detection unit with at least one detector delivering acquisition data, said client/server detection unit comprising:

self-triggering resources for reading the acquisition data so that the <u>client/server</u> detection unit is able to operate independently;

reading and processing resources independent of other nodes of the network; resources for transmission of the acquisition data via the <a href="Ethernet\_network">Ethernet\_network</a> to at least one other node (N);

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a clock unit allowing correlation between clocks of <u>the</u> multiple <u>client/server</u> detection units;

wherein said clock unit comprises:

resources for receiving a clock synchronization signal, generated by <u>each of the client/server detection unit</u> and including encoded instructions;

resources for transmission of an acknowledgment signal to <u>each of the</u> <u>client/server detection unit</u> transmitting the synchronization signal; and

resources for processing the encoded instructions, to increment an eventmarking sensor;

a detector performing a conversion of a physical magnitude into electrical signals delivered on several output paths;

a sequencer with resources performing:

sequencing for reading the clock synchronization signal from the clock unit;
sequencing for reading the acquisition data from the detector and configuration
data;

storage of the acquisition and configuration data;

analysis and processing of the acquisition data from the detector;

an interface to an <u>Ethernet</u> network processor, and the Ethernet network processor with resources providing:

an interface to the sequencer;

reception of the data sent by a user unit to perform the configuration of the detector and of the sequencer;

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processing of the acquisition data; and transmission of the acquisition data from the detector to the user unit.

12. (new) <u>The An installation system</u> according to claim 11, wherein the sequencer includes resources performing:

formatting of the acquisition data from the detector and of the information resulting from the processing effected by the detection unit;

storage in a memory of the processed and formatted acquisition data, and temporal marking of a trigger for acquisition of the data.

- 13. (new) <u>The An installation system</u> according to claim 12, wherein the sequencer is built around an FPGA device.
- 14. (new) <u>The An installation system</u> according to claim 11, wherein the Ethernet network processor includes resources performing:

retrieval of the data stored in the memory by the sequencer, analysis and processing of the said data, formatting of the processed data, and shared management of the data processing with other nodes of the network.

15. (new) <u>The An</u> installation <u>system</u> according to claim 12, wherein the sequencer performs the storage of the data in memory inside or outside the sequencer.

16. (new) <u>The An installation system</u> according to claim 11, wherein the detector includes: a sensitive sensor with a series of output paths,

a sub-module for reading the acquisition data, controlled by the sequencer and including a frontal electronic unit, and

a control sub-module managed by the sequencer to configure and control the frontal electronic unit.

17. (new) <u>The An installation system</u> according to claim 16, wherein the frontal electronic unit of the sub-module for reading includes:

resources for reading the acquisition data, resources for selection of an acquisition mode; resources for selection of an acquisition trigger source; and resources for amplification and shaping of signals, and resources for receiving configuration parameters.

- 18. (new) <u>The An installation system</u> according to claim 16, wherein the control sub-module includes resources to control the frontal electronic unit and to control the detector.
- 19. (new) The An installation system according to claim 11, wherein at least one of the nodes (N) of the Ethernet network constitutes a client/server user unit designed to provide the detection unit with configuration data from the unit, and to receive acquisition data transmitted by the detection unit.

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## Allowable Subject Matter

2. Claims 11-19 are allowed. The prior art failed to disclose a sequencer with resources performing: sequencing for reading the clock synchronization signal from the clock unit; sequencing for reading the acquisition data from the detector and configuration data; storage of the acquisition and configuration data; analysis and processing of the acquisition data from the detector, as recited in claim 11.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KAN YUEN whose telephone number is (571)270-1413. The examiner can normally be reached on Monday-Friday 10:00a.m-3:00p.m EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ricky O. Ngo can be reached on 571-272-3139. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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/Kan Yuen/ Examiner, Art Unit 2464 /Ricky Ngo/ Supervisory Patent Examiner, Art Unit 2464

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